

IN THE CLAIMS

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1. (Currently Amended) A phase synchronous multiple LC tank oscillator comprising:
a plurality of oscillator stages configured to oscillate synchronously, wherein:
the phase of oscillation of each of the plurality of oscillator stages is substantially the same;
a first oscillator stage among the plurality of oscillator stages that includes a first output connected to a second oscillator stage among the plurality of oscillator stages and a second output connected to a third oscillator stage among the plurality of oscillator stages.
2. (Original) A phase synchronous multiple LC tank oscillator as recited in claim 1 wherein the plurality of oscillator stages includes four oscillator stages, each oscillator stage having two inputs and two outputs and wherein each of the plurality of oscillator stages outputs its own output signal to two adjacent oscillator stages and also receives inputs from the two adjacent oscillator stages.
3. (Original) A phase synchronous multiple LC tank oscillator as recited in claim 1 wherein each of the plurality of oscillator stages has two inputs and two outputs and wherein each of the plurality of oscillator stages outputs its own output signal to two adjacent oscillator stages and also receives inputs from the two adjacent oscillator stages.
4. (Original) A phase synchronous multiple LC tank oscillator as recited in claim 1 wherein the plurality of oscillator stages includes four oscillators each oscillator stage a plurality of inputs and a plurality of outputs wherein each of the plurality of oscillator stages outputs its output signal to other oscillator stages in the plurality of oscillator stages and also receives inputs from the other oscillator stages in a manner that causes the oscillator stages to oscillate synchronously.
5. (Original) A phase synchronous multiple LC tank oscillator as recited in claim 1 wherein the plurality of oscillator stages includes an oscillator stage having a first output that is connected to an input of a first adjacent oscillator stage and a second output that is connected to an input of a second oscillator stage and wherein the oscillator stage further includes a first input that is

connected to an output of the first adjacent oscillator stage and a second input that is connected to an output the of the second adjacent oscillator stage.

6. (Original) A phase synchronous multiple LC tank oscillator as recited in claim 1 wherein the plurality of oscillator stages includes four oscillators each oscillator stage a plurality of inputs and a plurality of outputs wherein each of the plurality of oscillator stages outputs its output signal to other oscillator stages in the plurality of oscillator stages and also receives inputs from the other oscillator stages in a manner that causes the oscillator stages to oscillate synchronously and wherein each of the oscillator stages include corresponding inductors that are arranged to be mutually inductive.

7. (Original) A phase synchronous multiple LC tank oscillator as recited in claim 1 wherein each of the plurality of oscillator stages has greater than two inputs.

8. (Original) A phase synchronous multiple LC tank oscillator as recited in claim 1 wherein each of the plurality of oscillator stages has greater than two outputs.

9. (Original) A phase synchronous multiple LC tank oscillator as recited in claim 1 wherein each of the plurality of oscillator stages includes more than four oscillator stages.

10. (New) A phase synchronous multiple LC tank oscillator as recited in claim 1 wherein the LC tank oscillator is configured to generate an oscillating output signal.

11. (New) A phase synchronous multiple LC tank oscillator comprising:
a plurality of oscillator stages configured to oscillate synchronously; wherein:
the oscillator is configured to generate an oscillating output signal;
the number of oscillator stages is an integer N ; and
the oscillation phase difference between any two of the plurality of oscillator stages is less than $2\pi/N$.

INTERVIEW SUMMARY UNDER 37 CFR §1.133 AND MPEP §713.04

A telephonic interview in the above-referenced case was conducted on August 30, 2004 between the Examiner and the Applicant's undersigned representative. The Advisory Action mailed on July 29, 2004, was discussed. Specifically, proposed amendments to claims in light of US 6,717,478 (Kim et al.) and US 4,940,939 (Khait et al.) were discussed. The Applicant wishes to thank the Examiner for his time and attention in this case.